

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3383527	latch\$3 or shift\$3 or ff or (flip adj flop) or register or cell	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:20
L2	6866597	unload\$3 or direct\$2 or indirectly or select\$5 or unselect\$5 or skip\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:22
L3	4523427	mux\$3 or multiplex\$3 or select\$3 or deselect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:26
L4	189527	1 with 2 with (device or logic or structure or circuit\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:32
L5	1029751	3 near12 (device or logic or structure or circuit\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:31
L6	116606	4 same 5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:31
L7	135620	1 with 2 with 3 with (device or logic or structure or circuit\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:34
L8	91716	4.ti,ab,clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:34
L9	71640	(6 or 7) and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:34
L10	7077	(714/724,726,727,728,729,738,739,30).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:34
L11	699	(377/64,77,81).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:34
L12	1054	9 and (10 or 11)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:36
L13	77772	(chip or IC or (integrat\$3 adj circuit\$2)) near8 (diagnos\$4 or test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:38

## EAST Search History

L14	4565968	scan\$4 or shift\$3 or serial\$2 or series or sequen\$6 or cascad\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:39
L15	3010	(14 near15 1) with 13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:40
L16	1609	15.ti,ab,clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:41
L17	201	12 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/22 09:41